

HD74AC02

Quad 2-Input NAND Gate

REJ03D0240-0200Z (Previous ADE-205-356 (Z)) Rev.2.00 Jul.16.2004

Features

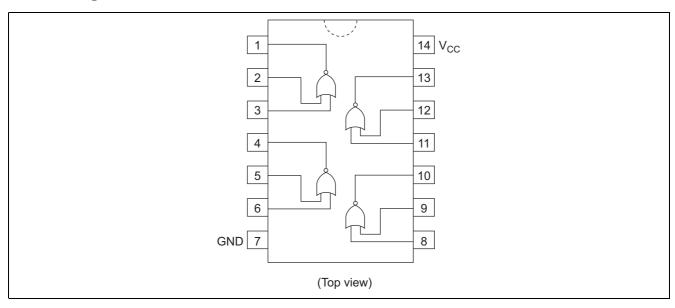
- Outputs Source/Sink 24 mA
- Ordering Information

Part Name	Package Type	Package Code	Package Abbreviation	Taping Abbreviation (Quantity)
HD74AC02P	DIP-14 pin	DP-14, -14AV	P	_
HD74AC02FPEL	SOP-14 pin (JEITA)	FP-14DAV	FP	EL (2,000 pcs/reel)
HD74AC02RPEL	SOP-14 pin (JEDEC)	FP-14DNV	RP	EL (2,500 pcs/reel)
HD74AC02TELL	TSSOP-14 pin	TTP-14DV	Т	ELL (2,000 pcs/reel)

Notes: 1. Please consult the sales office for the above package availability.

2. The packages with lead-free pins are distinguished from the conventional products by adding V at the end of the package code.

Pin Arrangement



Absolute Maximum Ratings

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	-0.5 to 7	٧	
DC input diode current	I _{IK}	-20	mA	$V_1 = -0.5V$
		20	mA	V _I = Vcc+0.5V
DC input voltage	V _I	-0.5 to Vcc+0.5	٧	
DC output diode current	I _{OK}	-50	mA	$V_0 = -0.5V$
		50	mA	$V_O = Vcc+0.5V$
DC output voltage	V _o	-0.5 to Vcc+0.5	٧	
DC output source or sink current	Io	±50	mA	
DC V _{cc} or ground current per output pin	I_{CC}, I_{GND}	±50	mA	
Storage temperature	Tstg	-65 to +150	°C	

Recommended Operating Conditions

Item	Symbol	Ratings	Unit	Condition
Supply voltage	V _{cc}	2 to 6	V	
Input and output voltage	V_{I}, V_{O}	0 to V _{cc}	V	
Operating temperature	Та	-40 to +85	°C	
Input rise and fall time	tr, tf	8	ns/V	$V_{CC} = 3.0V$
(except Schmitt inputs)				$V_{CC} = 4.5 \text{ V}$
V_{IN} 30% to 70% V_{CC}				V _{CC} = 5.5 V

DC Characteristics

Item	Sym- bol	Vcc (V)	Ta = 25°C		Ta = -40 to +85°C		Unit	Condition	
			min.	typ.	max.	min.	max.		
Input Voltage	V _{IH}	3.0	2.1	1.5		2.1	_	V	$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	3.15	2.25		3.15	—		
		5.5	3.85	2.75	—	3.85	—		
	V_{IL}	3.0	_	1.50	0.9	_	0.9		$V_{OUT} = 0.1 \text{ V or } V_{CC} - 0.1 \text{ V}$
		4.5	_	2.25	1.35	_	1.35		
		5.5	_	2.75	1.65	_	1.65		
Output voltage	V _{OH}	3.0	2.9	2.99	_	2.9	_	٧	$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	4.4	4.49	_	4.4	_		$I_{OUT} = -50 \mu A$
		5.5	5.4	5.49	_	5.4	_		
		3.0	2.58	_	_	2.48	_		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OH} = -12 \text{ mA}$
		4.5	3.94	_	_	3.80	_		$I_{OH} = -24 \text{ mA}$
		5.5	4.94	_	_	4.80	_		$I_{OH} = -24 \text{ mA}$
	V _{OL}	3.0	_	0.002	0.1	_	0.1		$V_{IN} = V_{IL} \text{ or } V_{IH}$
		4.5	_	0.001	0.1	_	0.1		I _{OUT} = 50 μA
		5.5	_	0.001	0.1	_	0.1		
		3.0	_	_	0.32	_	0.37		$V_{IN} = V_{IL} \text{ or } V_{IH}$ $I_{OL} = 12 \text{ mA}$
		4.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
		5.5	_	_	0.32	_	0.37		$I_{OL} = 24 \text{ mA}$
Input leakage current	I _{IN}	5.5	_	_	±0.1	_	±1.0	μА	$V_{IN} = V_{CC}$ or GND
Dynamic output	I _{OLD}	5.5	_	_	_	86	_	mΑ	V _{OLD} = 1.1 V
current*	I _{OHD}	5.5	_	_	_	-75	_	mΑ	V _{OHD} = 3.85 V
Quiescent supply current	I _{cc}	5.5	_	_	4.0	_	40	μА	$V_{IN} = V_{CC}$ or ground

^{*}Maximum test duration 2.0 ms, one output loaded at a time.



AC Characteristics

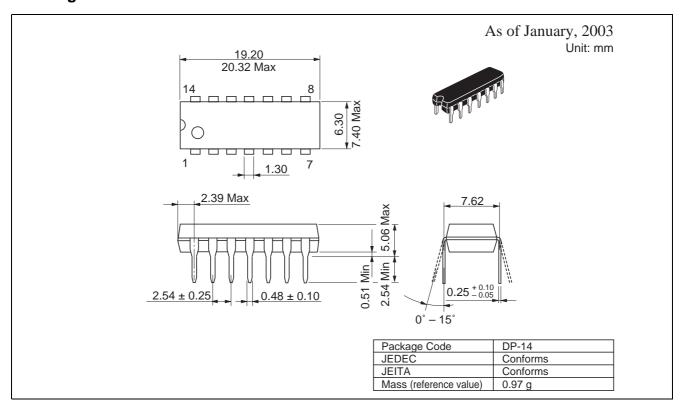
			Ta = +25°C C _L = 50 pF			C to +85°C 50 pF		
Item	Symbol	V _{cc} (V)*1	Min	Тур	Max	Min	Max	Unit
Propagation delay	t _{PLH}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.0	6.0	1.0	6.5	
Propagation delay	t _{PHL}	3.3	1.0	5.0	7.5	1.0	8.0	ns
		5.0	1.0	4.5	6.5	1.0	7.0	

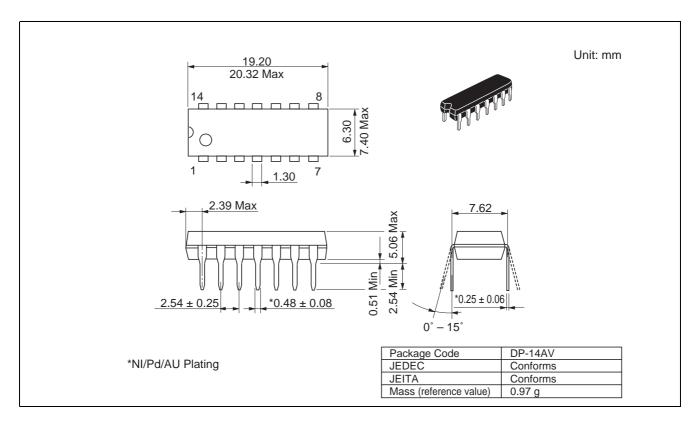
Note: 1. Voltage Range 3.3 is $3.3 \text{ V} \pm 0.3 \text{ V}$ Voltage Range 5.0 is $5.0 \text{ V} \pm 0.5 \text{ V}$

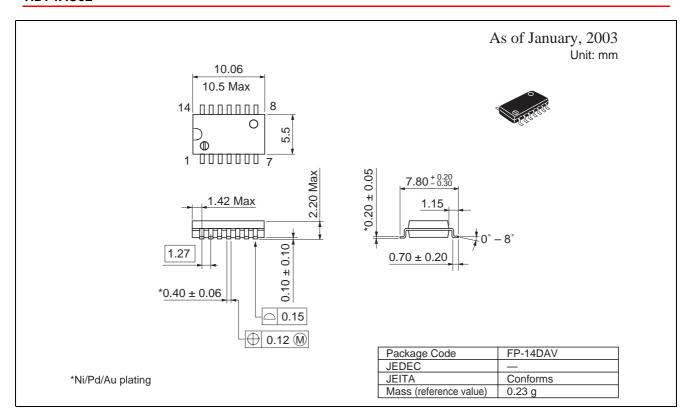
Capacitance

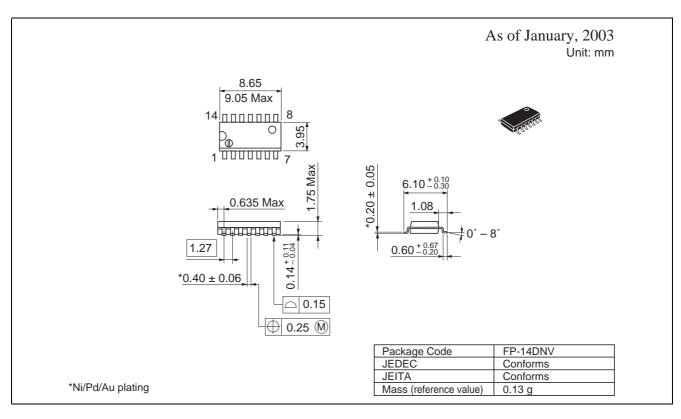
Item	Symbol	Тур	Unit	Condition
Input capacitance	C _{IN}	4.5	pF	V _{CC} = 5.5 V
Power dissipation capacitance	C _{PD}	30.0	pF	V _{CC} = 5.0 V

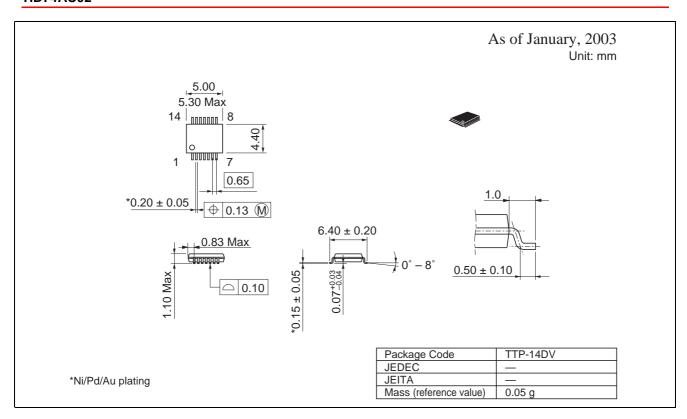
Package Dimensions











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